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Bib Data Sheet

CONFIRMATION NO. 6170

SERIAL NUMBER 10/823,872	FILING DATE 04/14/2004 RULE	CLASS 700	GROUP ART UNIT 2125	ATTORNEY DOCKET NO. 24061.77 (TSMC2003-0441)
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** CONTINUING DATA ***** *JNA*** FOREIGN APPLICATIONS ***** *JNA*

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** 06/23/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 5	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>JNA</i> Examiner's Signature _____ Initials _____				

ADDRESS

42717

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TITLE

System and method for identifying semiconductor process steps for queue-time control and abnormality detection

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